

Fig 2

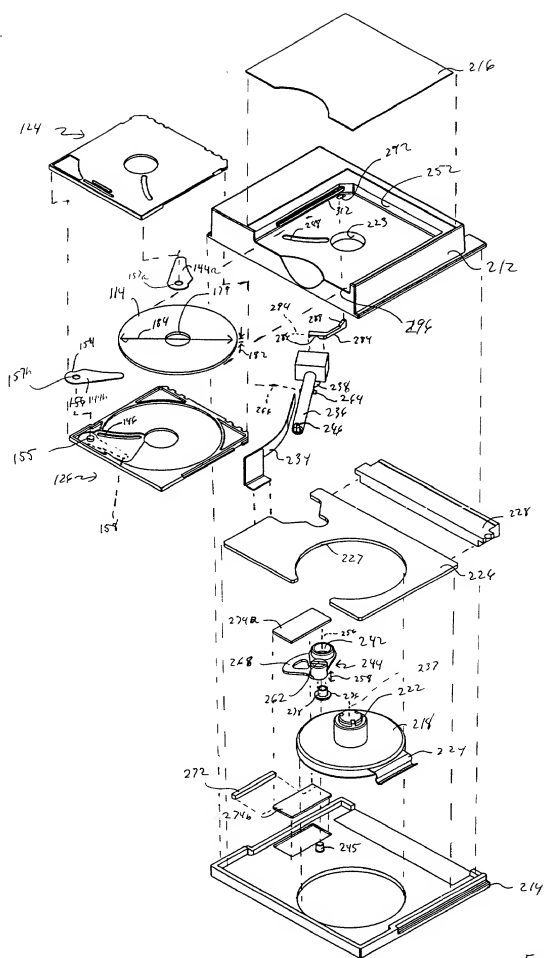
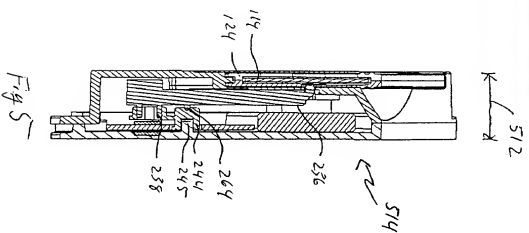
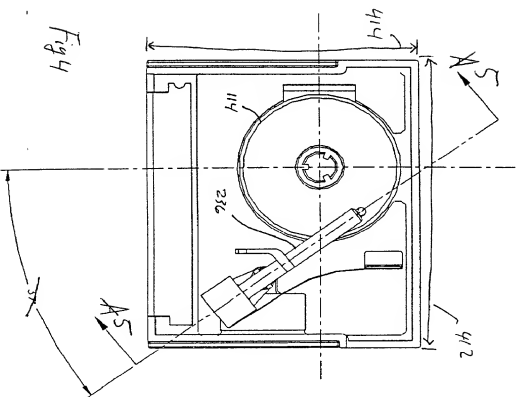
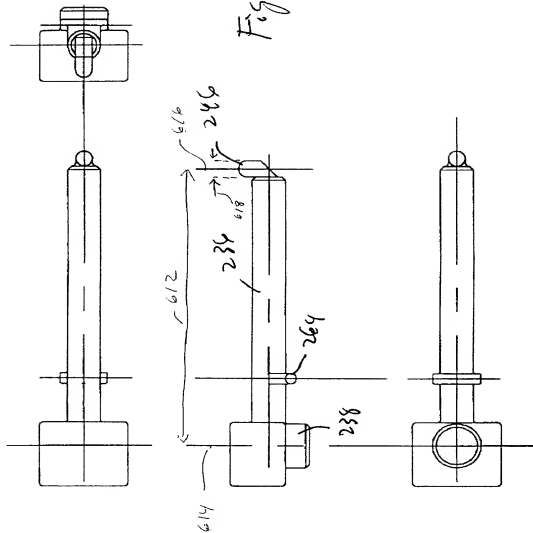
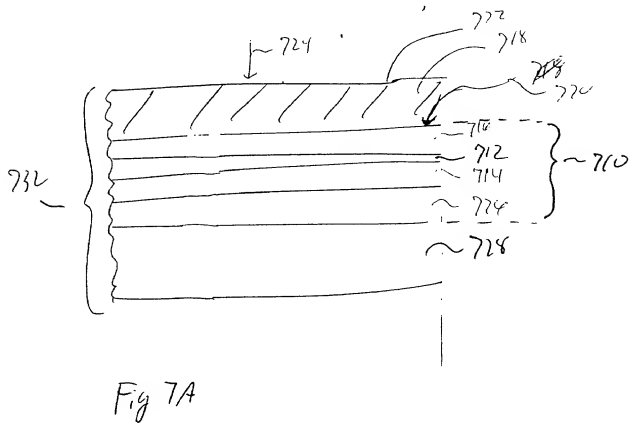
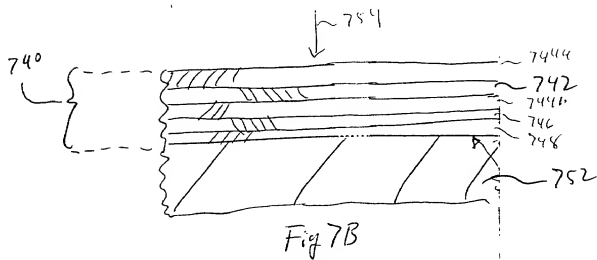
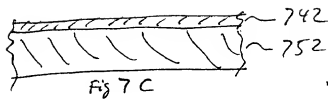
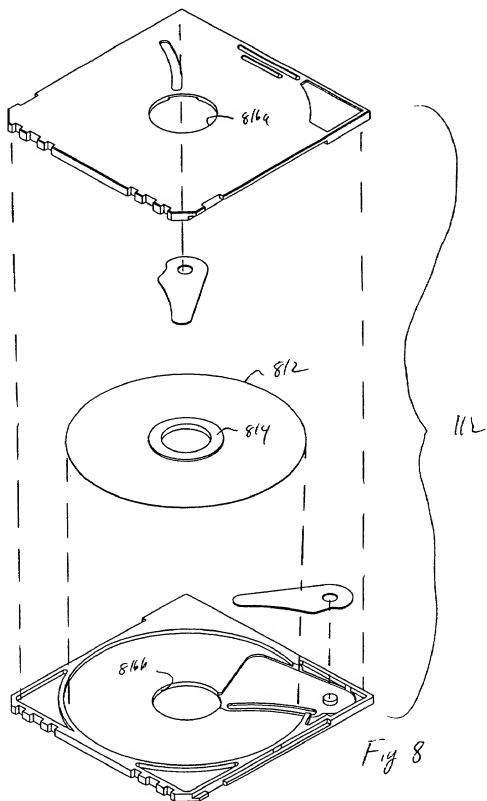


Fig 3









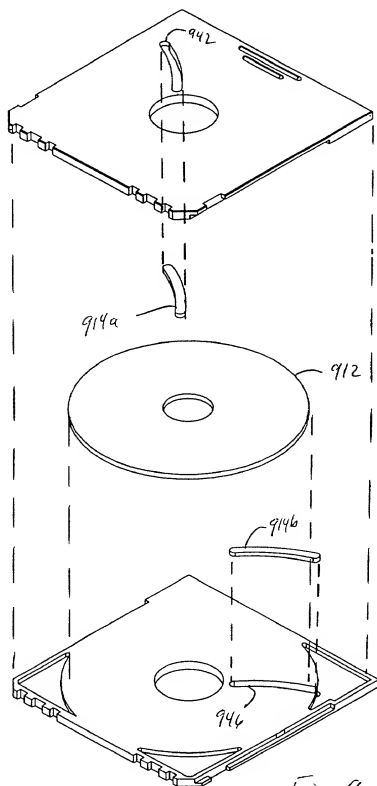


Fig 9

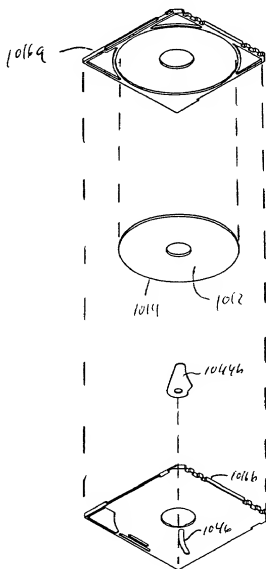


Fig 10A

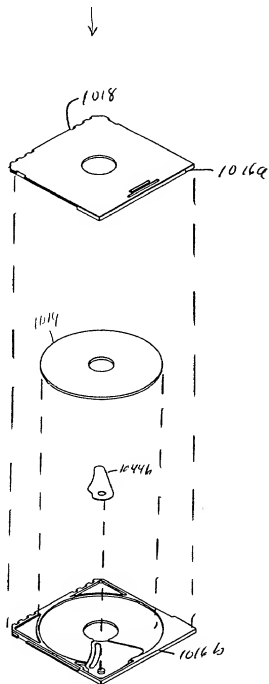


Fig 10B

(i)

Optical Head Electronics

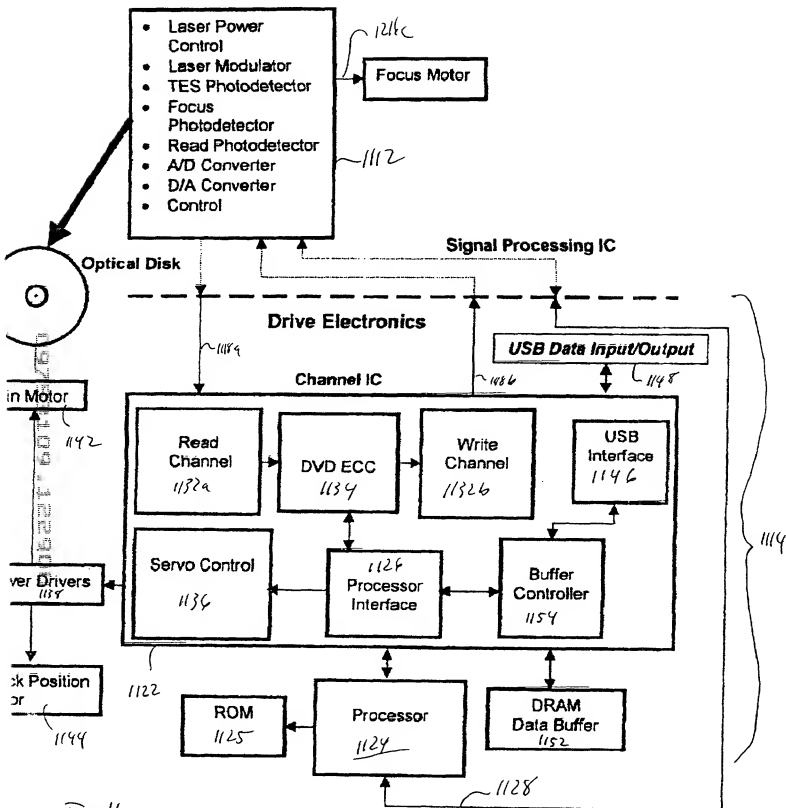
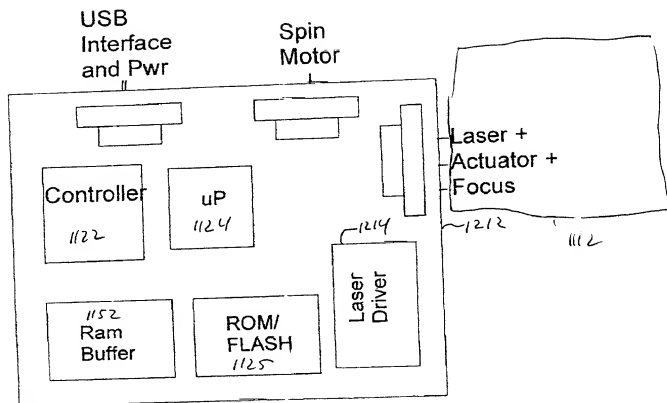


Fig 11



DataPlay PCBA

Fig 12

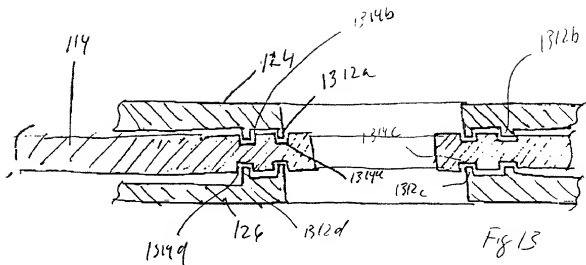


Fig 13

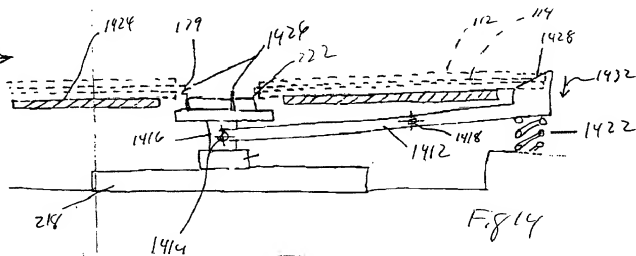


Fig 14

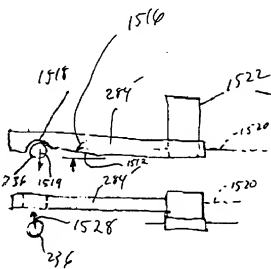


Fig 15a

Fig 15 b

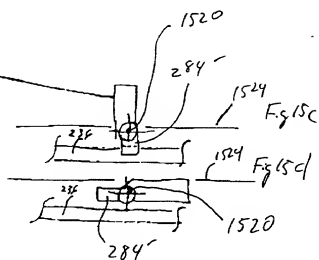


Fig 15c

Fig 15d

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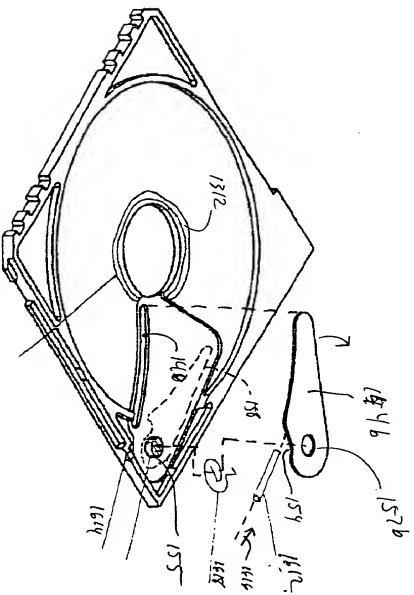
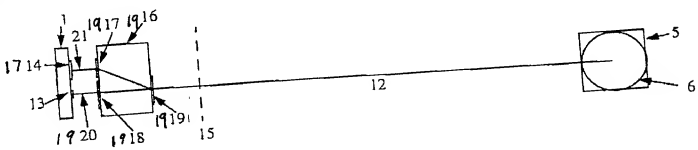
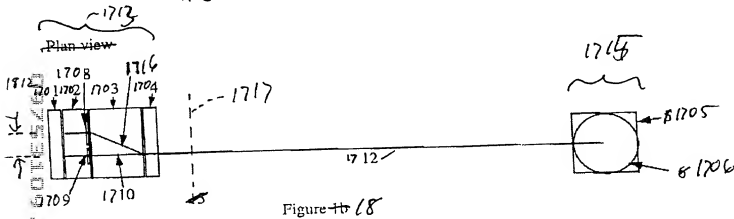
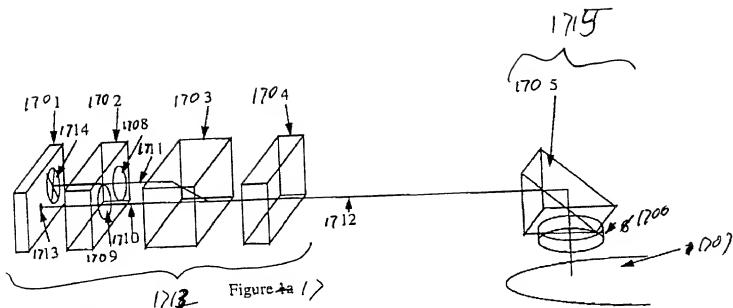
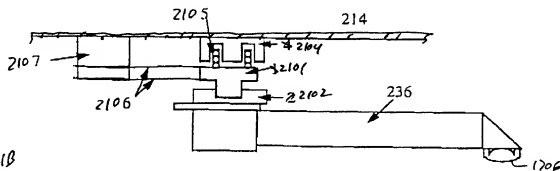
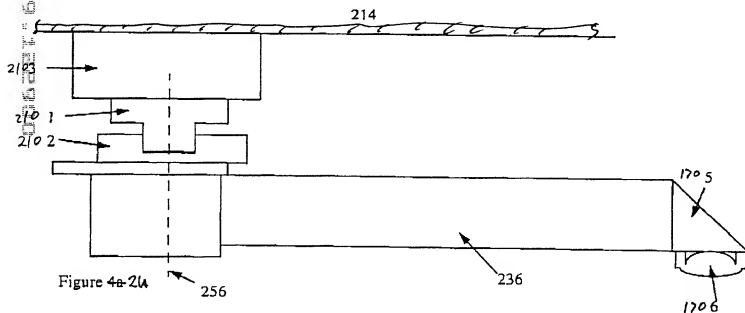
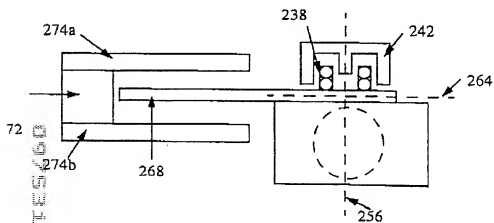
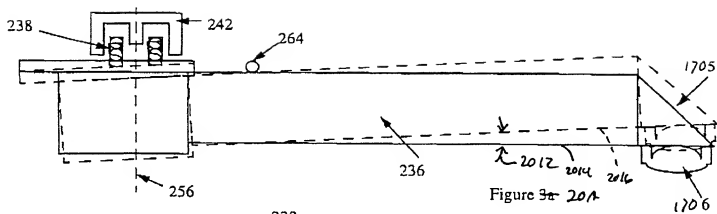
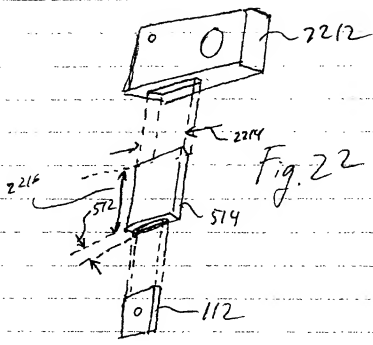


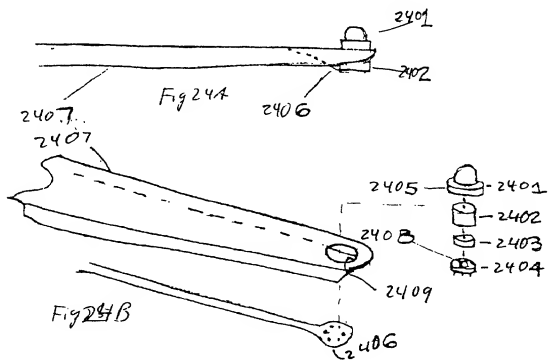
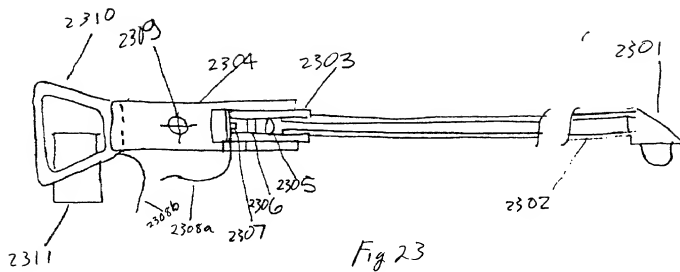
Fig. 16







00534704-122900



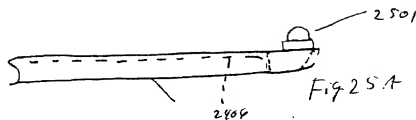


Fig 25A

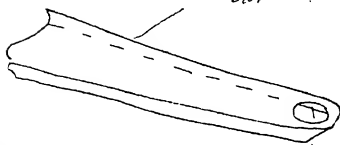


Fig 25B

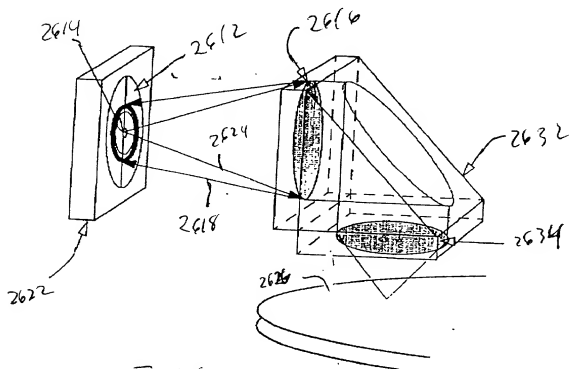
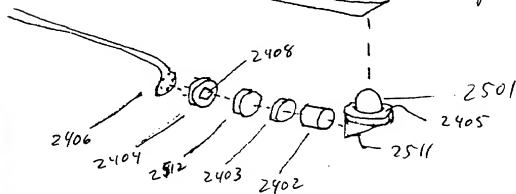


Fig 26

